Docket No.: 066365-0021



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Customer Number: 20277

Simon KNOWLES

Confirmation Number: 3801

Application No.: 10/813,433

Group Art Unit: 2183

Filed: March 31, 2004

Examiner: Ryan P. Fiegle

For: APPARATUS AND METHOD FOR SEPARATE ASYMMETRIC CONTROL PROCESSING AND DATA PATH PROCESSING IN A DUAL PATH PROCESSOR

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection or Notice of Allowance.

In accordance with 37 CFR 1.17(p), please charge the fee or f 1.180.00 to Deposit A 5804171 10813433 No. 500417.

10/813,433

The references US 2002/010852, US 2002/174266, US 6,061,367, US 2002/063577, and

the reference by Alippi, et al. were cited in a corresponding foreign application search report

and their relevance is discussed therein. A copy of the foreign search report is attached for the

Examiner's information.

The references US 5,600,810, US 2003/0154358 A1, and the reference by BEEBE, et

al. were first cited on the form PTO-1449 dated September 1, 2006 of corresponding US Patent

Application No. 10/813,628. The references US 6,725,357, US 6,880,150, US 5,956,518, and

the references by SIMONEN, et al. and TANENBAUM were first cited on the form PTO-892

dated May 15, 2006 of corresponding US Patent Application No. 10/813,628.

The references US 5,423,051, US 2005/0044434 A1, and the references by

BOLOTSKI, et al. and STOKES were first cited on the form PTO-892 dated May 22, 2006 of

corresponding US Patent Application No. 10/813,615.

Please charge any shortage in fees due in connection with the filing of this paper,

including extension of time fees, to Deposit Account 500417 and please credit any excess fees to

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Respectfully submitted,

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2

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SHEET 1 OF 1

SERIAL NO. INFORMATION DISCLOSURE ATTY, DOCKET NO. 066365-0021 10/813,433 CITATION IN AN APPLICATION **APPLICANT** Simon KNOWLES FILING DATE **GROUP** (PTO-1449) March 31, 2004 2183 U.S. PATENT DOCUMENTS **EXAMINER'S** CITE **Document Number** Publication Date Name of Patentee or Applicant of Cited Pages, Columns, Lines, Where MM-DD-YYYY **INITIALS** Document Relevant Passages or Relevant Number-Kind Code2 (if known) Figures Appear US 06-06-1995 5,423,051 FULLER et al. US 2005/0044434 A1 02-24-2005 KAHLE et al. US 2003/0154358 A1 08-14-2003 SEONG et al. บร 02-04-1997 5,600,810 OHKAMI US 6,725,357 B1 04-20-2004 COUSIN บร 6.880,150 B1 04-12-2005 TAKAYAMA et al. บร 09-21-1999 DEHON et al. 5,956,518 US 2002/0010852 A1 01-24-2002 ARNOLD et al. 11-21-2002 us 2002/0174266 A1 PALEM et al. υs 6.061.367 05-09-2000 SIEMERS บร 2002/0063577 A1 05-30-2002 ABBOTT FOREIGN PATENT DOCUMENTS **EXAMINER'S** Foreign Patent Document **Publication Date** Name of Patentee or Pages, Columns, Lines Translation INITIALS Applicant of Cited Document Where Relevant Country Codes - Number 4 - Kind CITE MM-DD-YYYY Yes No Codes (if known) Figures Appear NO. OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) **EXAMINER'S** Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where INITIALS CITE BOLOTSKI, et al., "Unifying FPGAs and SIMD Arrays," M.I.T. Transit Project, February 8, 1994, pp. 1-22, Transit Note #95. STOKES, "A Brief Look at the PowerPC 970," Ars Technica, 2002, pp. 1-3. BEEBE, et al., "Instruction sequencing control," IBM Technical Disclosure Bulletin, May 1972, pp. 3599-3611, Vol. 14, No. 12. SIMONEN, et al., "Variable-Length Instruction Compression for Area Minimization," IEEE, 2003. TANENBAUM, "Structured Computer Organization," 1984, pp. 10-12, Prentice-Hall, Inc., Englewood Cliffs, NJ. ALIPPI, et al., "Determining the Optimum Extended Instruction-Set Architecture for Application Specific Reconfigurable VLIW CPUs," IEEE, 2001, pp. 50-56. International Search Report issued in corresponding International Application No. PCT/GB2005/001073, dated April 5, 2006

DATE CONSIDERED

EXAMINER

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.